



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2836
#5
a
9-3-2

In re Application of:
MCCORD, DON

Serial No. 09/980,509

Filed: **OCTOBER 19, 2001**

For: **METHOD AND SYSTEM FOR WAFERS
AND DEVICE-LEVEL TESTING OF AN
INTEGRATED CIRCUIT**

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Attorney Docket No. **37802PCT-US**

Examiner: **UNKNOWN**

Group Art Unit **2836**

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D. C. 20231

Sir:

Applicant requests that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above-entitled application, pursuant to 37 C.F.R. 1.97. This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) within three months of the filing date of the application, or before the mailing date of a first office action on the merits. No fee or certification is required.

CERTIFICATE OF MAILING

37 CFR 1.8(A)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Commissioner for Patents, Washington, D. C. 20231.

8/22/02
Date

[Signature]
Signature

No fees are believed to be necessary. Please charge any fees that may be necessary for prosecution of the present application to **Bracewell & Patterson, L.L.P. Deposit Account No. 50-0259.**

Respectfully submitted,



Brian F. Russell

Registration No. 40,796

BRACEWELL & PATTERSON, LLP

Suite 350 Lakewood on the Park

7600B North Capital of Texas Hwy.

Austin, Texas 78731

512-343-6116

ATTORNEY FOR APPLICANTS

